

Form PTO-149 <i>APR 01 2002</i> <i>U.S. Patent and Trademark Office</i>		Docket No. P01,0365	Serial No. 10/040,395				
		Applicant Yue et al					
		Filing Date January 7, 2002	Group Art Unit				
<b>U.S. PATENT DOCUMENTS</b>							
Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
	AA						
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<b>FOREIGN PATENT DOCUMENTS</b>							
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							Yes
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<b>OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
<i>SR</i>	AR	Analysis and Optimization of Accumulation-Mode Varactor for RF ICs - Soorapanth et al 1998 Symposium on VLSI Circuits Digest of Technical Papers, pp. 32-33, June 1998					
<i>SR</i>	AS	UTSi CMOS: A Complete RF SOI Solution - Analog Integrated Circuits and Signal Processing, 25, 171-179, 2000 Kluwer Academic Publishers					
<i>SR</i>	AT	Design of High-Q Varactors for Low-Power Wireless Applications Using a Standard CMOS Process - Porret et al - IEEE Journal of Solid-State Circuits, Vol. 35, No. 3, March 2000					
<i>SR</i>	AU	A Metal-Oxide-Semiconductor Varactor - Svelto et al - IEEE Electron Device Letters, Vol. 20, No. 4 April 1999					
	AV						
Examiner	<i>Shoayee</i>		Date Considered	<i>9-19-03</i>			
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